

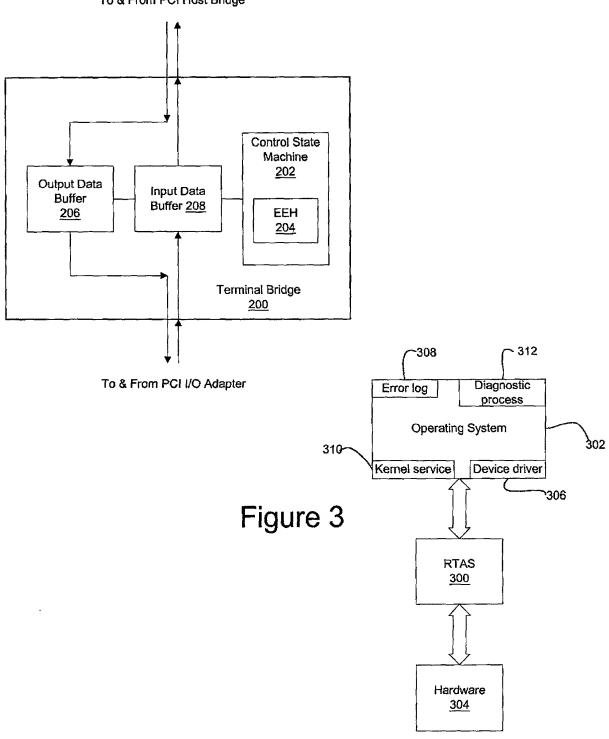
Figure 1

Arnot et al.
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Figure 2

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To & From PCI Host Bridge



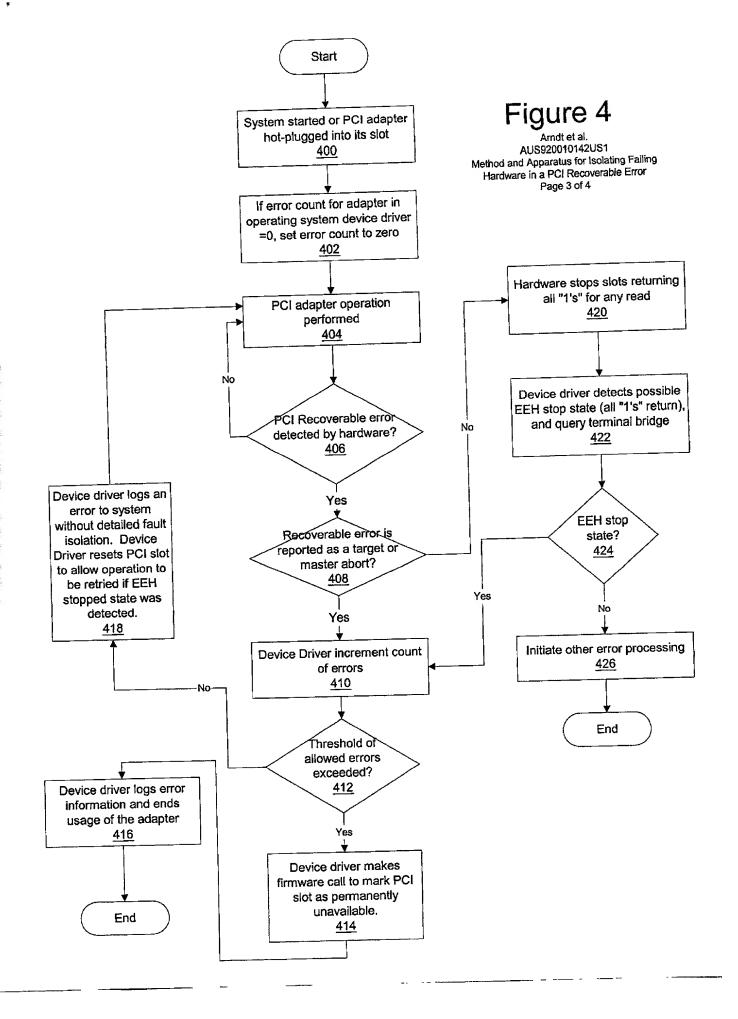


Figure 5

Arndt et al.
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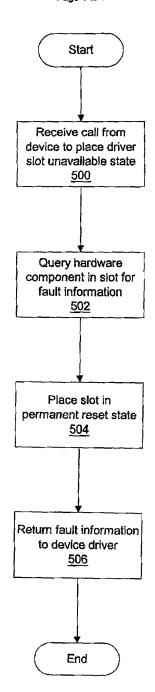


Figure 6

